



Prescription

An introduction to the design and construction of digital electronic instruments. Following a review of binary arithmetic and Boolean algebra, the course will focus on the design of digital circuits using both combinatorial and sequential logic. Further work will study microprocessor architectures, programming and interfacing and the conversions of digital and analogue signals.

Course learning objectives

Students who pass this course should be able to:

1. Describe the properties, construction and operating characteristics of digital integrated circuits from the most important CMOS Logic families. (BE graduate attribute 3(a))
2. Use the understanding of the basic logic operations and logic circuit elements to create digital circuits. (BE graduate attribute 3(a,b))
3. Design synchronous sequential circuits. (BE graduate attribute 3(b,c))
4. Explain the basic architecture of a microcontroller (BE graduate attribute 3(a))
5. Program a microprocessor in assembly language to implement an embedded system. ((BE graduate attribute 3(a, b))

Course content

This course is a lab-based course. Labs and tests will require in-person attendance by students in the Wellington Region. There will be online options for students outside the Wellington region, but students will have to complete alternative substitutes to the physical lab requirements. The remote option for tests will use a Zoom-based system for online supervision of the tests.

Students taking this course remotely from outside the Wellington region must have access to a computer with camera and microphone and a reliable high speed internet connection that will support real-time video plus audio connections and screen sharing. Students must be able to use Zoom; other communication applications may also be used. A mobile phone connection only is not considered sufficient. The computer must be adequate to support the practical work required by the course: almost any modern windows, macintosh, or unix laptop or desktop computer will be sufficient, but an Android or IOS tablet will not.

Withdrawal from Course

Withdrawal dates and process:

<https://www.wgtn.ac.nz/students/study/course-additions-withdrawals>

Lecturers

Hamish Colenso (Coordinator)

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CO 251 Cotton Building (All Blocks), Gate 7, Kelburn Parade, Kelburn

Dr Robin Dykstra

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AM 415 Alan Macdiamid Building, Gate 7, Kelburn Parade, Kelburn

Teaching Format

The course will be taught through lectures and tutorials and with a strong focus on laboratory design skills.

Student feedback

Student feedback on University courses may be found at:
www.cad.vuw.ac.nz/feedback/feedback_display.php

Dates (trimester, teaching & break dates)

- Teaching: 28 February 2022 - 03 June 2022
- Break: 11 April 2022 - 24 April 2022
- Study period: 06 June 2022 - 09 June 2022
- Exam period: 10 June 2022 - 25 June 2022

Class Times and Room Numbers

28 February 2022 - 10 April 2022

- **Monday** 13:10 - 14:00 – 202, New Kirk, Kelburn
- **Wednesday** 13:10 - 14:00 – 202, New Kirk, Kelburn
- **Thursday** 13:10 - 14:00 – 202, New Kirk, Kelburn

25 April 2022 - 05 June 2022

- **Monday** 13:10 - 14:00 – 202, New Kirk, Kelburn
- **Wednesday** 13:10 - 14:00 – 202, New Kirk, Kelburn
- **Thursday** 13:10 - 14:00 – 202, New Kirk, Kelburn

Set Texts and Recommended Readings

Required

There are no required texts for this offering.

Mandatory Course Requirements

There are no mandatory course requirements for this course.

If you believe that exceptional circumstances may prevent you from meeting the mandatory course requirements, contact the Course Coordinator for advice as soon as possible.

Assessment

This course is 100% internally assessed.

Assessment Item	Due Date or Test Date	CLO(s)	Percentage
Two assignments	TBC	CLO: 1,2,3	10%
Laboratory work (3 hrs per week)	TBC	CLO: 2,3,5	40%
Two Tests (90 minutes duration each)	TBC	CLO: 1,2,3,4,5	50%

Penalties

All work is due in on the due date. Marks will be deducted at a rate of 10% of the full mark for each working day late. Work will not be marked if more than 1 week late.

Extensions

Extensions must be requested in writing (email) and will only be given in exceptional circumstances, and if agreed before the due date. No late work will be accepted after the model solutions to any piece of assessment have been distributed to the class.

Submission & Return

All submissions are to be electronic and via the ECS online submission system.

Workload

The student workload for this course is 150 hours.

Teaching Plan

The following material will be covered during EEEN 202:

Overview of logic gates, combinatorial logic, Boolean algebra, simplification, K-maps
Sequential logic, Flip-Flops, Counters, asynchronous and synchronous,
Registers, Arithmetic Circuits
Design of synchronous counters, state machines
Hardware Description Language
Microprocessor architecture and operation based on 8051,
Timing, polling and interrupts
Analog to digital conversion,
Memory Devices

Communication of Additional Information

Course materials and other information will be available from https://ecs.wgtn.ac.nz/Courses/EEEN202_2022T1/. Students should check there regularly.

Links to General Course Information

- Academic Integrity and Plagiarism: <https://www.wgtn.ac.nz/students/study/exams/integrity-plagiarism>
- Academic Progress: <https://www.wgtn.ac.nz/students/study/progress/academic-progress> (including restrictions and non-engagement)
- Dates and deadlines: <https://www.wgtn.ac.nz/students/study/dates>
- Grades: <https://www.wgtn.ac.nz/students/study/progress/grades>
- Special passes: Refer to the Assessment Handbook, at <https://www.wgtn.ac.nz/documents/policy/staff-policy/assessment-handbook.pdf>
- Statutes and policies, e.g. Student Conduct Statute: <https://www.wgtn.ac.nz/about/governance/strategy>
- Student support: <https://www.wgtn.ac.nz/students/support>
- Students with disabilities: https://www.wgtn.ac.nz/st_services/disability/
- Student Charter: <https://www.wgtn.ac.nz/learning-teaching/learning-partnerships/student-charter>
- Terms and Conditions: <https://www.wgtn.ac.nz/study/apply-enroll/terms-conditions/student-contract>
- Turnitin: <http://www.cad.vuw.ac.nz/wiki/index.php/Turnitin>
- University structure: <https://www.wgtn.ac.nz/about/governance/structure>
- VUWSA: <http://www.vuwsa.org.nz>

Offering CRN: [33054](#)

Points: 15

Prerequisites: one of (COMP 102, 112, ENGR 101, 121, MATH 161)

Restrictions: ECEN 202

Duration: 28 February 2022 - 26 June 2022

Starts: Trimester 1

Campus: Kelburn