

EEEN203 Analogue Circuits and Systems

Lab 4 SPICE 2

Due: Due 10:00 a.m., Monday 8 May 2023

1 Introduction

In this lab you will use LTspice to explore some of the properties of RLC resonant circuits.

You will also be introduced to some advanced LTspice tips and tricks that should be useful as you continue your studies in EEEN203.

2 Background

Resonance is a common topic that pops up throughout Electronic and Computer Systems Engineering. It plays a central part in filter theory (EEEN220, 301, 303), is used in oscillator and positive feedback circuits (303, 403) and even features prominently in mechatronics, often as the result of mechanical, rather than electrical, interactions (425).

We are also familiar with resonance in our every day lives: musical instruments have specially designed sound boxes to help their strings *resonate*, while near-field payment systems such as Snapper, payWave, and PayPass rely on resonance to power embedded microchips that otherwise would have no power source.

We will use LTspice to illustrate some of the properties of resonant circuits in this lab.

Recall that you can use LTspice on the computers in the labs by running
C:\Program Files\LTC\LTspiceIV\XVIIx64.exe.

Alternatively, you can install LTspice on your own machine by downloading the relevant package from linear.com/designtools/software/#LTspice. It is cross-platform, available free of charge, and does not have a node limit.

Also recall that should you get stuck and require help with LTspice, there is a wealth of information available online. In particular, there is an official getting started guide at cds.linear.com/docs/en/software-and-simulation/LTspiceGettingStartedGuide.pdf. Simon Bramble also maintains a series of excellent tutorials on LTspice (simonbramble.co.uk/lt_spice/ltspice_lt_spice.htm), Part 1 is most relevant for EEEN203.

3 Resonant Frequency

Consider the LTspice model of a series RLC circuit shown in Figure 1. Notice that it contains an independent voltage source, set to give an AC signal with an amplitude of 1 and a phase of 0: AC 1 0.

- Calculate the transfer function of this circuit, i.e., the ratio of the voltage across the resistor, $R1$, compared to the source voltage, $V1$.

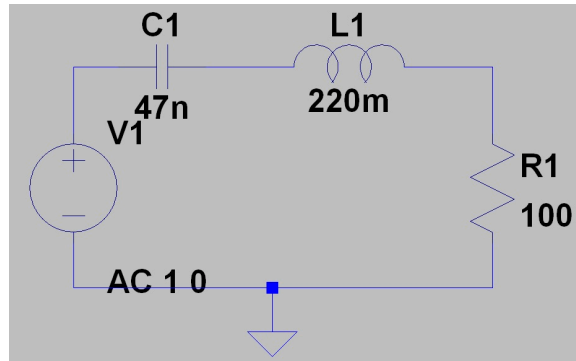


Figure 1: Series RLC circuit.

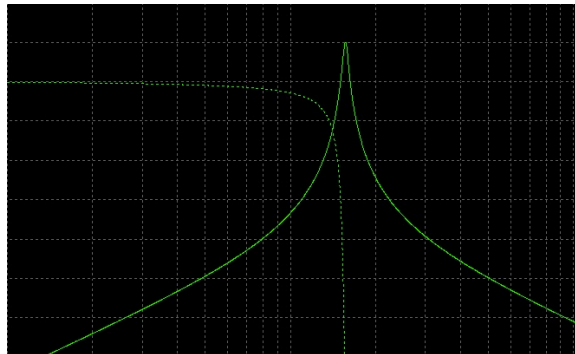


Figure 2: The shape of the frequency response of a resonant circuit. Solid line is magnitude, dotted is phase, but none of that matters because there aren't any axes labels or coordinates on this figure!

- Knowing the circuit transfer function, derive an expression for the resonant frequency of the circuit, i.e., the frequency at which the imaginary component of the transfer function is minimised.
- Recreate the circuit shown in Figure 1 in LTspice and run an AC analysis on it. Start with 3 points per decade and choose your start and end frequency to be roughly three decades either side of your expected resonant frequency.
- Plot the voltage across R1 and use cursors¹ to compare the resonant frequency of your simulated circuit to the resonant frequency you calculated.
- Does your simulated frequency response look similar to the one shown in Figure 2? Do your measurements of resonant frequency agree with your calculations? If not, try increasing the number of discrete frequencies modelled in your simulation. You can do this by increasing the number of points per decade in the “Edit Simulation Cmd” dialogue box. How many points are necessary to get a transfer function as smooth as the one shown in Figure 2? And how many points are necessary to get agreement between your calculated and measured values of resonance?
- Before you start the next section, save your LTspice model file and create a copy to work on. You will need the original again later in the lab.

4 Q Factor

The Q - or quality-factor of a resonant circuit is a measure of how much energy is stored in the circuit during each AC cycle. Specifically, it is the ratio of energy stored to energy dissipated during each cycle. For a series RLC circuit,

$$Q_{\text{series cct}} = \frac{1}{R} \sqrt{\frac{L}{C}} \quad (1)$$

¹Click on the name of the trace at the top of the plot to bring up the cursor dialogue box.

The form of the equation changes slightly for a parallel resonant circuit, to

$$Q_{\text{parallel cct}} = R\sqrt{\frac{C}{L}} \quad (2)$$

For either circuit, we can measure Q as the ratio of the resonant frequency, f_{res} , to the bandwidth², B , of the circuit:

$$Q = \frac{f_{\text{res}}}{B} \quad (3)$$

Practically, circuits with high Q factor tend to have smaller bandwidths and thus higher selectivity.

- Calculate the Q factor for the circuit shown in Figure 1. Compare this value to a measured value based on the transfer function you simulated in the previous section. Does your simulated value agree with your calculations?
- Calculate the value of the resistor, R1, required to give you Q values of 0.25, 0.5, 0.707, 1, and 10.
- Change the independent voltage source to provide a DC amplitude of 1 V and run a transient analysis on your model, making sure to select the “Start external DC supply voltage at 0V” checkbox. A stop time of about 100ms should be long enough for this particular analysis.

Note the characteristics of the voltage waveform across R1 when the resistor has a value of 100 Ω .

- Now vary the simulated value of R1 according to the values you calculated to give predetermined Q . Observe the changes in the voltage waveform across R1.

Hint: rather than examining waveforms for each value of R1 individually, it may be useful to calculate all of the curves in a single simulation run.

1. To do this, set the value of R1 to {R}: this tells the model that R1 is equal to the value of the variable, R.
2. Next, click on the .op button in LTspice to enter a SPICE directive: `.step param R list 1 10 100`. Note the leading period! This directive tells the model that the parameter R will have three separate values of 1, 10, and 100, which are specified as a list.
3. Rerun your analysis.

- What is the effect of increasing Q on the transient response of your circuit?

We say that circuits with low Q factor, i.e. $Q < 0.5$, are *overdamped*.

Similarly, circuits with $Q > 0.5$ are considered *underdamped*, and circuits with $Q = 0.5$ are *critically damped*.

Based on the voltage waveforms you observed across R1, what do you think these terms (overdamped, underdamped, critically damped) mean in the context of RLC circuits?

Are there any properties that you can say belong specifically to overdamped or underdamped systems?

5 Parameter sensitivity to component variations in resonant circuits

While simulation with ideal components can guide the design of electronic circuits, we need to be mindful that real components do not have exact values and that the results we obtain in the real world can be quite different from those we simulate on a PC.

For example, we are already well aware that resistors have particular tolerances associated with them, as specified by the fourth or fifth (depending on the resistor) colour band on their body.

We know that resistors with a gold tolerance band may vary by up to 5%, while resistors with a brown band can be expected to vary by only 1% from their nominal value.

²Recall that the bandwidth of a circuit is the range of frequencies for which the gain is within 3 dB of the maximum gain.

Fortunately, LTspice can help us out in some cases where we expect component variations to be critical to parameters in our design.

For this part of the lab, we are going to use the LTspice model constructed at the start of the lab to run. We will run Monte Carlo analyses, which allow us to determine the variation we can expect in parameters such as resonant frequency and Q factor from variations in the nominal values of our R1, L1, and C1 components.

- We will explore the effect of variations in capacitance first. Change the value of the capacitor in your model to `{mc(47n,0.10)}`.

Here `mc` is short for Monte Carlo, which tells the model that the component value of the capacitor is nominally 47 nF with a uniformly distributed random variation in the range $\pm 5\%$.

- Next, add a SPICE directive to complete several runs of the simulation, rather than just one:

```
.step param run 1 100 1
```

Here the numbers after the parameter, `run`, are the start value, 1, the end value, 100, and the increment: 1. Notice that we could have used the `list` syntax from the previous section, but this would have been tedious:

```
.step param run list 1 2 3 4 5 6 7...
```

- Run your simulation and estimate the range of resonant frequencies produced. Similarly, estimate the range of bandwidths, and calculate the range of Q factors.

Hint: to get the best estimates you may wish to increase the number of frequencies modelled around the resonant frequency. So that the addition of frequency points doesn't slow your simulations down too much, consider reducing the overall range of frequencies you simulate for this question.

- What is the relationship between changes in the capacitor component value, C , and the parameters f_{res} and Q ? For example, does f_{res} vary with C , C^2 , $1/C$ or $C^{1/2}$?
- Repeat your analysis separately for changes in L and R. Do the variations in f_{res} and Q make sense given the expression for f_{res} you derived in Section 3 and the equation given for $Q_{\text{series cct}}$ in Section 4, i.e. (1)?

In the comparisons you have just performed you have been assessing with the sensitivity S , of parameter P to component C . In this case the components are the values of the resistor, capacitor, and inductor in your RLC circuit and the parameters are f_{res} and Q , but we can use the same concept to assess a range of components and parameters far beyond this narrow example. Formally, we define the sensitivity of a parameter to a component, S_C^P , as

$$S_C^P = \frac{dP}{P} / \frac{dC}{C} \quad (4)$$

This means that, for example, the sensitivity of a parameter that varies by 10% for a component that changes by 10% is 1, and the sensitivity of a parameter that varies by 1% for a component variation of 10% is 0.1.

Generally smaller variations are better when we are dealing with unintended sensitivities.

6 Extension

If you finish the lab and have time to spare, consider some of the following questions:

- How does the transfer function for a series RLC circuit that you calculated in Section 3 change if the circuit contains a resistor, capacitor, and inductor all in parallel across a voltage source? At resonance, is the current drawn by the parallel circuit at a maximum or a minimum? At frequencies far away from the resonant frequency, is the voltage high or low?
- What is the sensitivity of f_{res} when both L1 and C1 are subject to variation? How does this compare to the sensitivities of f_{res} to just L1 or C1 in isolation?

- What happens to Q in your series RLC circuit as R_1 shrinks to zero? What does this do to the step response³ of the circuit? What would happen if Q were negative? How could you practically make Q negative?

7 Conclusion

This laboratory introduced various LTspice techniques that allow a series RLC circuit to be modelled.

We have seen that simulations will agree closely with calculations provided they are set up correctly: often it is not obvious to know if this is the case.

We have also seen that aspects of a model can be given several different values by a single LTspice simulation.

This last property should be of special interest to us as we begin to use LTspice in more and more of our engineering courses.

8 To hand in

You need to hand in a brief write-up including an Aim, a Method, and Results.

1. Aim: a short introduction describing and motivating the task (why is it important),
2. Methods: a theoretical section describing in more detail the task at hand and the approach (the methods) taken (this section usually includes equations), and
3. Results: a section that describes the results, and a discussion of what they mean. The results section should have three subsections (one for each of Sections 3, 4, and 5 of lab script), and enough of your working to show that you've completed the lab. Each will include quite a few screen shots.

³You calculated the step response of your RLC circuit in Section 4.

Marking schedule (out of 9):

1. Aim: a short introduction describing and motivating the task (why is it important), (1 mark)
2. Methods: a theoretical section describing in more detail the task at hand and the approach (the methods) taken (this section usually includes equations), (1 mark) and
3. Results: a section that describes the results, and a discussion of what they mean. (2 marks for each of sections 3, 4 and 5)

1 mark for overall presentation and structure.