

ENGR 101 Engineering Technology

Assignment 3

Due date: Wednesday, 3 June 2024 (23:59 hrs)

Design a logic circuit for an elevator in a four storey (ie four floors/levels) building.



Use the five steps discussed in the lectures to solve the above problem. You must show all the working/calculations for each of the five steps.

NOTE:

You must type your answers using a word processor or any other similar computer program. Save the computer file containing your answers as a pdf file using the filename format: **Student Id number-A3.pdf**. For example, 1812401234-A2.pdf