

EXAMINATIONS – 2019

TRIMESTER 2

SWEN 430

COMPILER ENGINEERING

Time Allowed: TWO HOURS

CLOSED BOOK

Permitted materials: No calculators permitted.
Non-electronic Foreign language to English dictionaries are allowed.

Instructions: Answer all questions

You may answer the questions in any order. Make sure you clearly identify the question you are answering.

Question	Topic	Marks
1.	Grammars and Parsing	20
2.	Types and Type Checking	20
3.	Static Analysis	20
4.	Java Bytecode	20
5.	Machine Code	20
6.	Advanced Topics	20
Total		120

1. Grammars and Parsing

(20 marks)

(a) (6 marks)

Briefly describe the *two* conditions a context-free grammar must satisfy in order to be considered LL(1) (i.e. suitable for a recursive descent parser).

Condition 1:

Condition 2:

- (b) Consider the following grammar, where nonterminals are in italics, terminals are enclosed in double quotes, *id* denotes an identifier, and $\langle \text{empty} \rangle$ denotes an empty string.

Header ::= *RPart* id "(" *APart* ")"

RPart ::= id | $\langle \text{empty} \rangle$

APart ::= id | id "," *APart*

- i. (8 marks) Explain the ways in which this grammar violates the LL(1) conditions, and how they would affect the behaviour of a recursive descent parser based on this grammar.

- ii. (6 marks) Write an equivalent LL(1) grammar.

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2. Types and Type Checking

(20 marks)

(a) (12 marks)

For each of the following kinds of errors, say whether that kind of error can be detected by a type checker in a strongly typed language, and **explain your answer**.

(i) Adding an integer to a Boolean value.

(ii) Calling a function or method with the wrong number of arguments.

(iii) Division by zero.

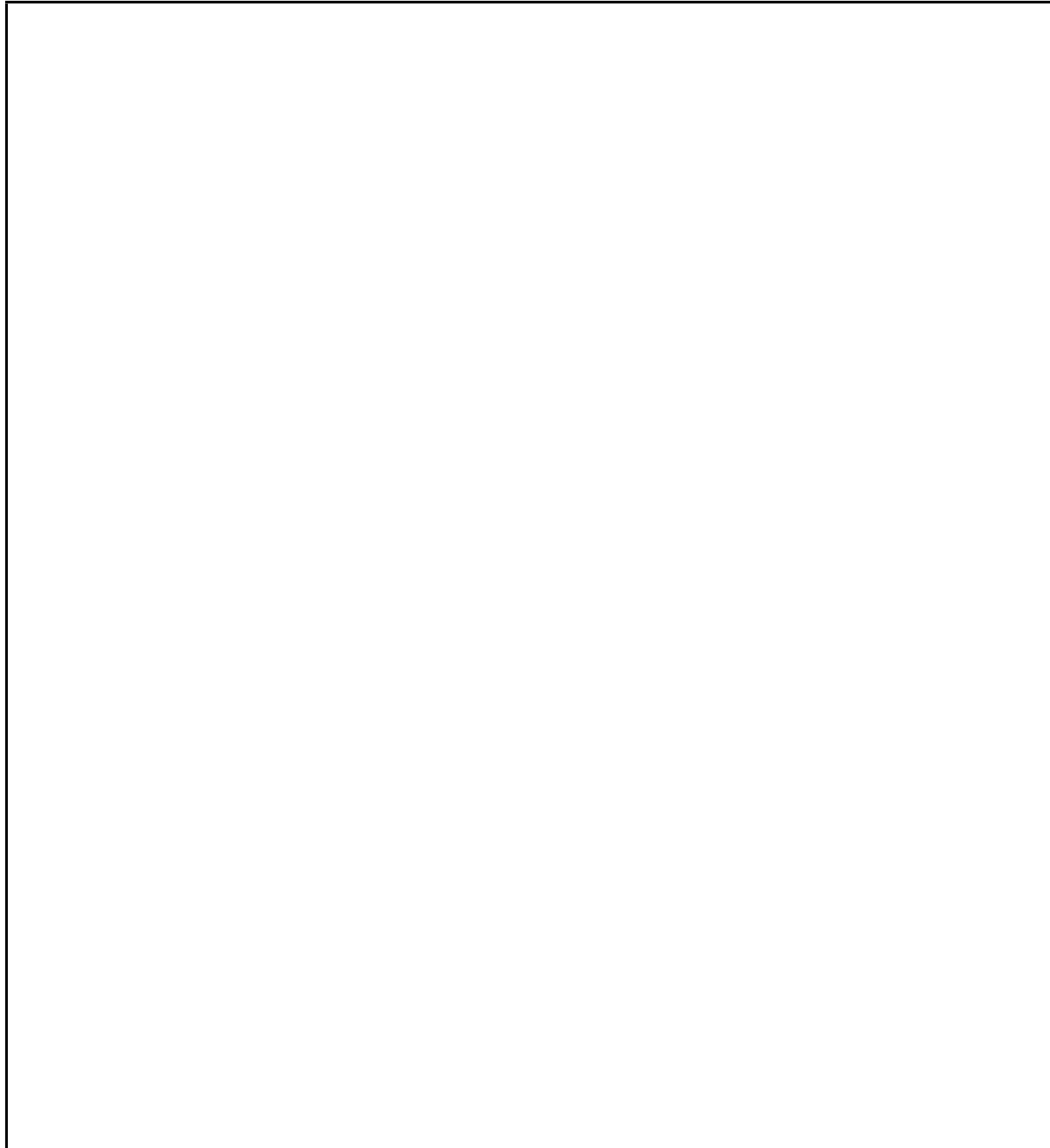
(iv) Calling a non-existent method on an object.

(v) Missing case label in a `switch` statement.

(vi) Dereferencing a `null` pointer.

(b) (8 marks)

Adding union types to a programming language increases the expressiveness of the language, but makes type checking more complicated. Discuss the main issues that arise in testing for type equivalence and subtype compatibility in the presence of union types.



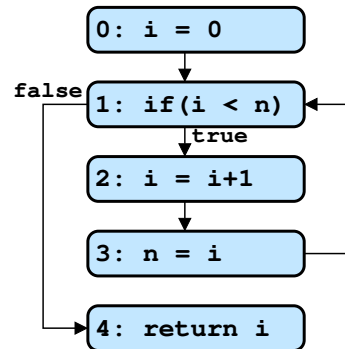
3. Static Analysis

(20 marks)

The *definite unassignment* phase is used in Java to check that **final** variables are only assigned once. The following illustrates:

```

1  int aMethod(final int n) {
2      int i = 0;
3      while(i < n) {
4          i = i + 1;
5          n = i;
6      }
7      return i;
8  }
    
```

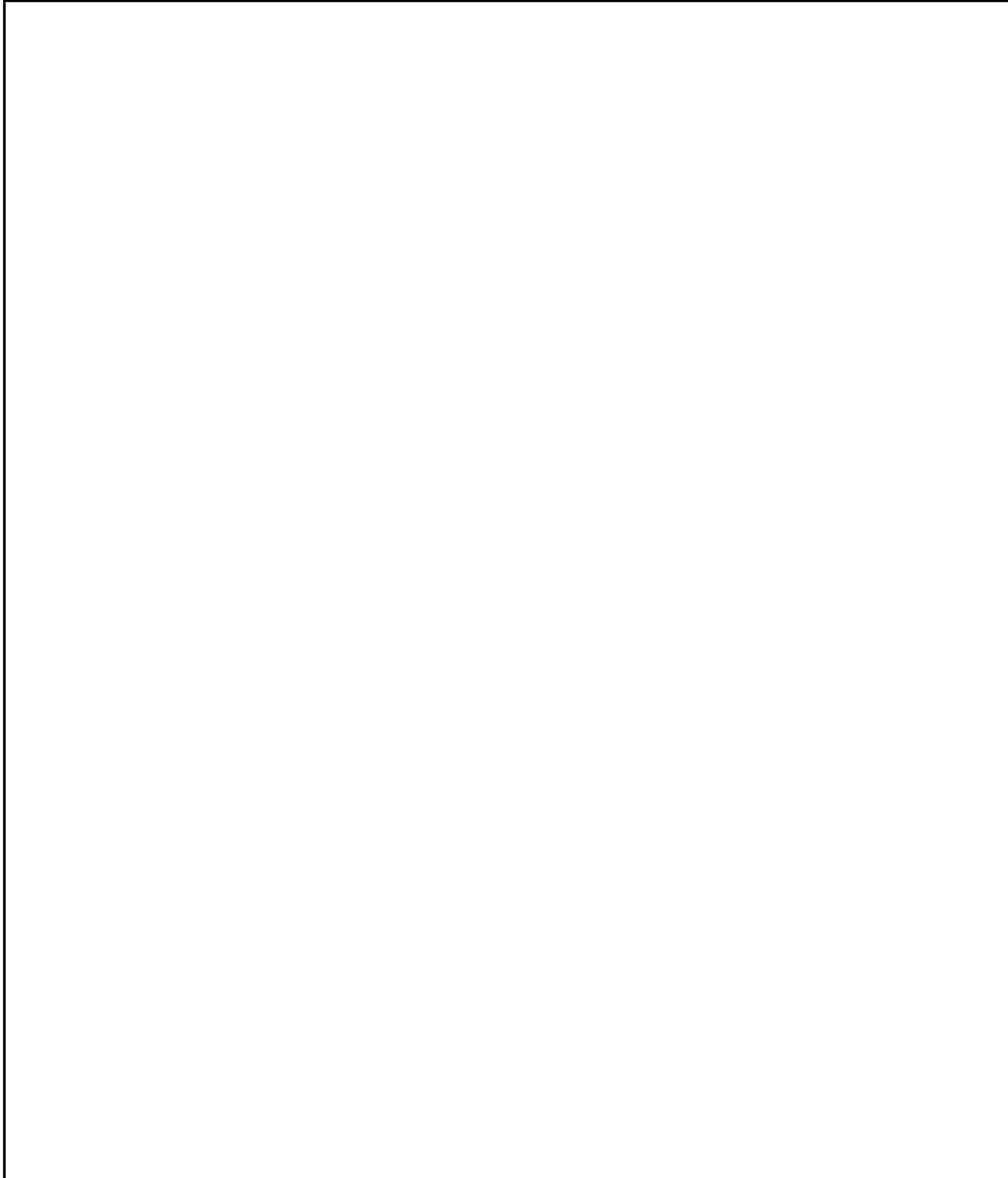


The above method fails definite unassignment because the **final** variable *n* may be assigned more than once. The definite unassignment algorithm determines, at each point, which variables may have been assigned at that point.

- (a) (5 marks) Explain briefly, using an example, why no algorithm accurately can detect *all* cases of definite unassignment.

- (b) (5 marks) Using the `aMethod()` example above, explain briefly why a depth-first traversal algorithm is *insufficient* for checking definite unassignment.

- (c) **(10 marks)** Briefly, outline how an algorithm for detecting definite unassignment would work. You may give the *dataflow equations* if this helps.



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4. Java Bytecode

(20 marks)

(a) Consider the following method written in Java bytecode:

```
public int f(int []);  
  0:  iconst_0  
  1:  istore_2  
  2:  iconst_0  
  3:  istore_3  
  4:  iload_2  
  5:  aload_1  
  6:  arraylength  
  7:  if_icmpge 23  
10:  iload_3  
11:  aload_1  
12:  iload_2  
13:  iaload  
14:  iadd  
15:  istore_3  
16:  iload_2  
17:  iconst_1  
18:  iadd  
19:  istore_2  
20:  goto 4  
23:  iload_3  
24:  ireturn
```

i. (5 marks) In the box below, give Java source code equivalent to the bytecode above:

NOTE: Appendix A on p19 provides an overview of bytecode instructions for reference.

- ii. (3 marks) What is the *maximum stack height* of the above method? Be sure to show your working by indicating below the height at each point.

```
public int f(int[]);
  0: iconst_0
  1: istore_2
  2: iconst_0
  3: istore_3
  4: iload_2
  5: aload_1
  6: arraylength
  7: if_icmpge 23
 10: iload_3
 11: aload_1
 12: iload_2
 13: iaload
 14: iadd
 15: istore_3
 16: iload_2
 17: iconst_1
 18: iadd
 19: istore_2
 20: goto 4
 23: iload_3
 24: ireturn
```

- (b) For each of the following JVM error messages, briefly discuss what might have caused the problem. You may use examples to illustrate as necessary.

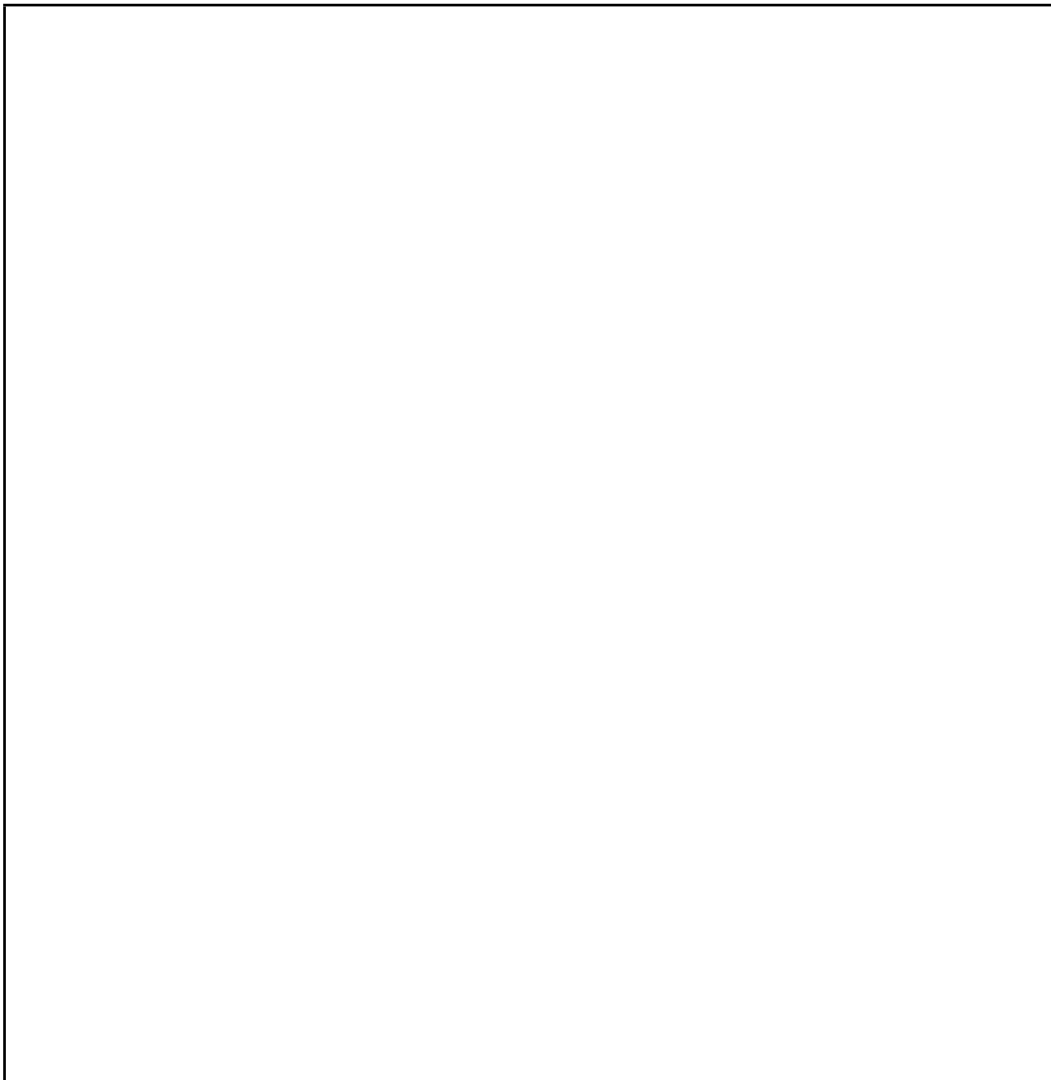
- i. (2 marks) “Unable to pop operand off an empty stack”

- ii. (2 marks) “Accessing value from uninitialized register”

- iii. (2 marks) “Inconsistent stack height”

(c) (6 marks) Translate the following method into Java bytecode:

```
1  public static int fib(int n) {  
2      if(n == 0 || n == 1) { return n; }  
3      else {  
4          return fib(n - 1) + fib(n - 2);  
5      }  
6  }
```



5. Machine Code

(20 marks)

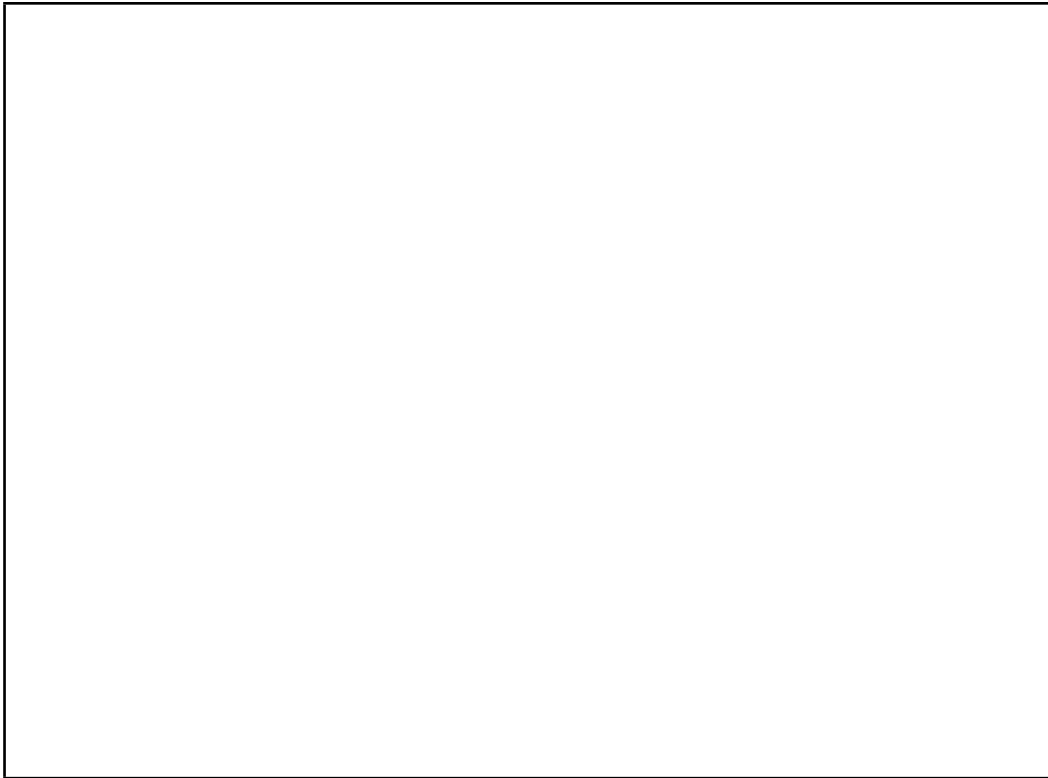
Consider the following function, `mul`, written in `x86_64` assembly language:

```
1  mul:
2      pushq %rbp
3      movq %rsp, %rbp
4      subq $16, %rsp
5      movq $0, %rax
6      movq %rax, -8(%rbp)
7      movq $0, %rax
8      movq %rax, -16(%rbp)
9  L1:
10     movq -16(%rbp), %rax
11     movq 32(%rbp), %rbx
12     cmpq %rbx, %rax
13     jge L2
14     movq -8(%rbp), %rax
15     movq 24(%rbp), %rbx
16     addq %rbx, %rax
17     movq %rax, -8(%rbp)
18     movq -16(%rbp), %rax
19     movq $1, %rbx
20     addq %rbx, %rax
21     movq %rax, -16(%rbp)
22     jmp L1
23  L2:
24     movq -8(%rbp), %rax
25     movq %rax, 16(%rbp)
26     movq %rbp, %rsp
27     popq %rbp
28     ret
```

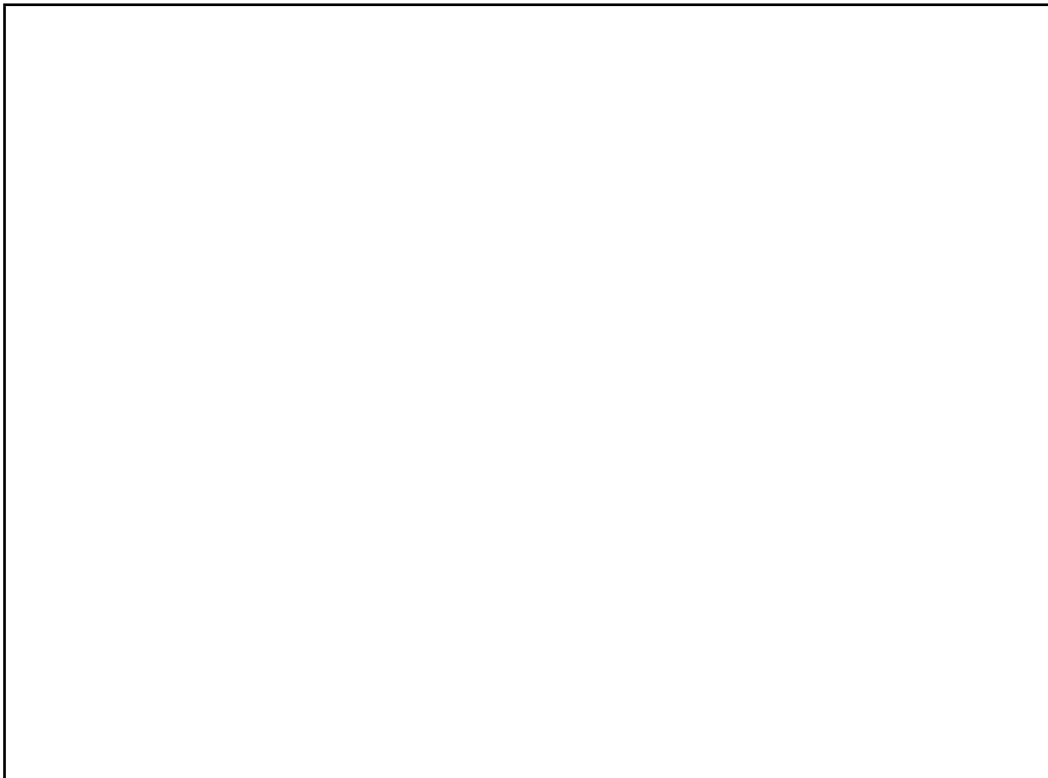
NOTE: the Appendix on page 20 provides an overview of `x86_64` machine instructions for reference.

- (a) (5 marks) Function parameters are normally passed *on the stack* or *in registers*. How are parameters passed in the above function? Justify your answer.

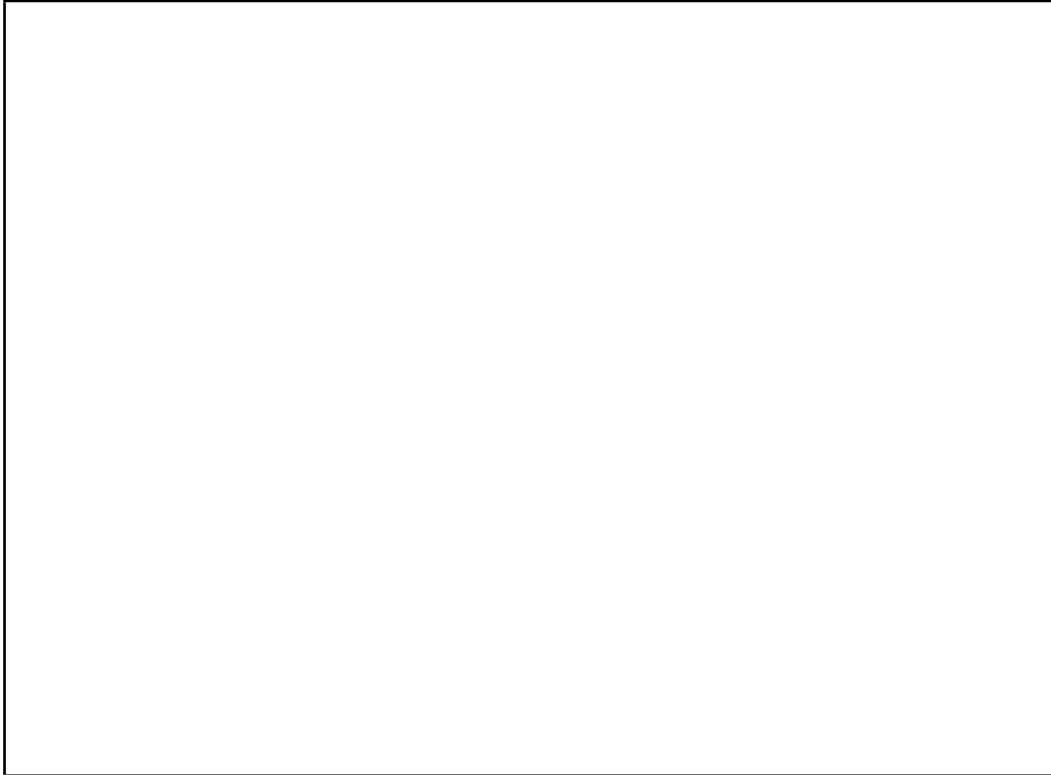
(b) (5 marks) Translate the `mul` function into `WHILE`.



(c) (5 marks) During execution, *stack frames* are created to hold critical information. Briefly, discuss the *stack frame layout* for the `mul` function using diagrams to illustrate.



- (d) **(5 marks)** The implementation of `mul` is not efficient. For example, it uses more machine instructions than necessary. Briefly, discuss how it can be rewritten to improve efficiency.



6. Advanced Topics

(20 marks)

(a) (10 marks)

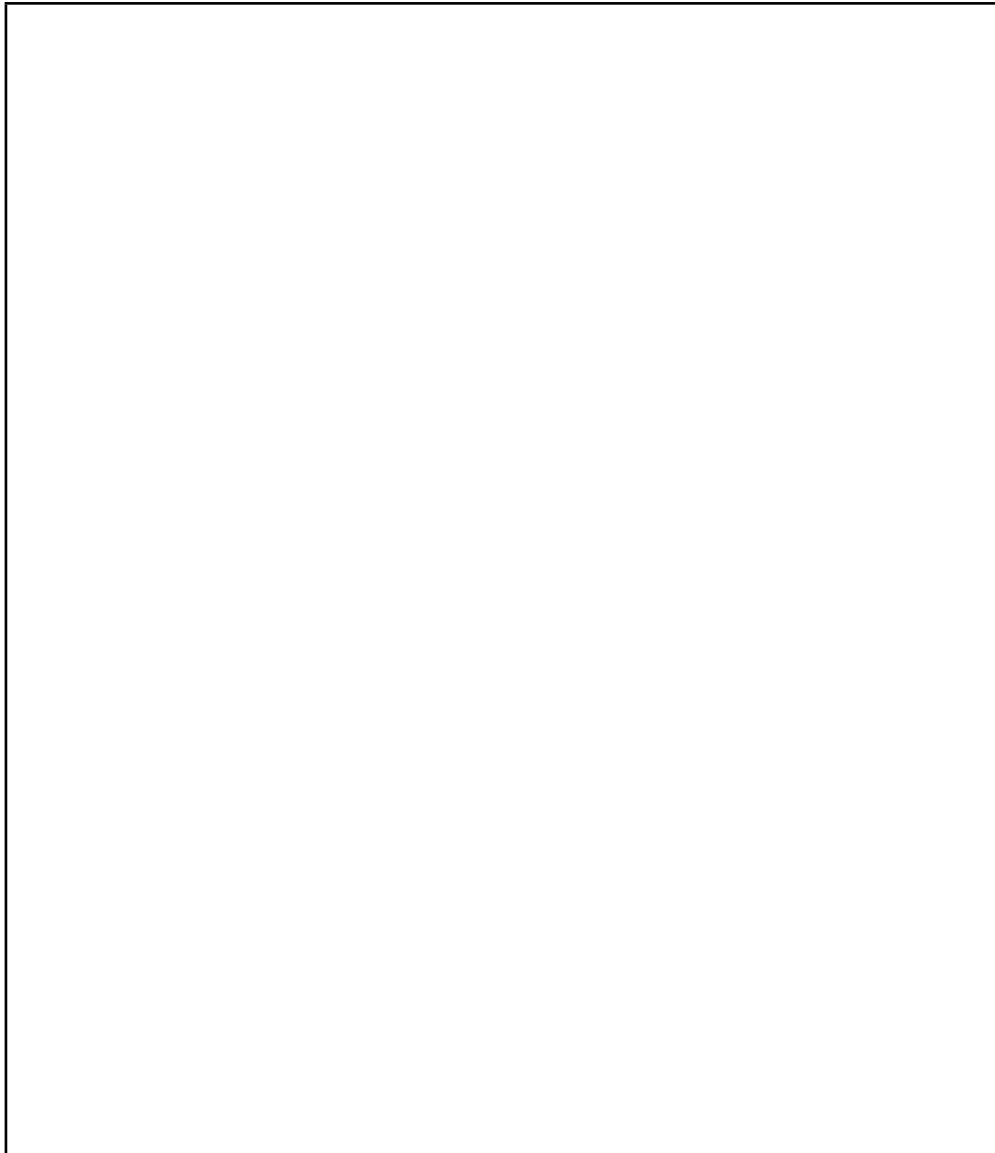
- (i) Briefly explain how implementing method calls in an object-oriented language differs from implementing function calls in a language like C, and why method calls can potentially be less efficient than C-like function calls.

- (ii) Discuss how static analysis techniques can be used to analyse method declarations and calls in an object-oriented program, and use this information to improve the efficiency of method calls.

(b) **(10 marks)**

Programmers tend to think of their programs as executing on a relatively simple computer, such as a PDP11, and many compiler optimisations are based on similar assumptions.

Discuss some of the ways in which modern machines differ from this simple model, and the impact that this has for code generation and optimisation in a compiler.



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Appendix A: Java Bytecodes

aaload	Load reference element from array onto stack.	..., aref, index \Rightarrow ..., ref
aastore	Store reference element into array from stack.	..., ref, index, val \Rightarrow ...
aload <i>n</i>	Load reference from local variable <i>n</i> onto stack.	... \Rightarrow ..., ref
areturn	Return reference from method.	..., ref \Rightarrow ...
arraylength	Push array length on stack.	..., aref \Rightarrow ..., int
astore <i>n</i>	Store reference into local variable <i>n</i> from stack.	..., ref \Rightarrow ...
bipush <i>c</i>	Load integer byte constant <i>c</i> onto stack.	... \Rightarrow ..., int
dup	Duplicate top item on stack.	..., val \Rightarrow ..., val, val
iadd	Add two ints on stack.	..., int, int \Rightarrow ..., int
iaload	Load int element from array onto stack.	... ref, index \Rightarrow ... val
iastore	Store int element into array from stack.	... ref, index, val \Rightarrow ...
iconst_c	Load integer constant <i>c</i> onto stack.	... \Rightarrow ..., int
idiv	Divide two ints on stack.	..., int, int \Rightarrow ..., int
iload <i>n</i>	Load int from local variable <i>n</i> onto stack.	... \Rightarrow ..., int
imul	Multiply two ints on stack.	..., int, int \Rightarrow ..., int
ineg	Negate int on stack.	..., int \Rightarrow ..., int
invokeinterface	Invoke interface method.	..., oref[val, [val, ...]] \Rightarrow [val]
invokespecial	Invoke special instance method (e.g. initialisation).	..., oref[val, [val, ...]] \Rightarrow [val]
invokestatic	Invoke static method.	... [val, [val, ...]] \Rightarrow [val]
invokevirtual	Invoke instance method.	..., oref[val, [val, ...]] \Rightarrow [val]
ireturn	Return int from method.	..., int \Rightarrow ...
istore <i>n</i>	Store int into local variable <i>n</i> from stack.	..., int \Rightarrow ...
isub	Subtract two ints on stack.	..., int, int \Rightarrow ..., int
if<cond>	Branch if int comparison with zero succeeds.	..., int \Rightarrow ...
if_acmp<cond> <i>d</i>	Branch to <i>d</i> if reference comparison succeeds.	..., ref, ref \Rightarrow ...
if_icmp<cond> <i>d</i>	Branch to <i>d</i> if int comparison succeeds.	..., int, int \Rightarrow ...
ldc <i>c</i>	Load constant (e.g. integer or string) <i>c</i> on stack.	... \Rightarrow ..., int
new <i>C</i>	Create a new object of class <i>C</i> \Rightarrow ..., ref
goto <i>d</i>	Branch unconditionally to <i>d</i> \Rightarrow ...
pop	Pop top item off stack.	..., val \Rightarrow ...
return	Return from method.	... \Rightarrow ...
sipush <i>c</i>	Load integer word constant <i>c</i> onto stack.	... \Rightarrow ..., int

Appendix B: x86_64 Machine Instructions

<code>movq \$c, %rax</code>	Assign constant <code>c</code> to <code>rax</code> register
<code>movq %rax, %rdi</code>	Assign register <code>rax</code> to <code>rdi</code> register
<code>addq \$c, %rax</code>	Add constant <code>c</code> to <code>rax</code> register
<code>addq %rax, %rbx</code>	Add <code>rax</code> register to <code>rbx</code> register
<code>subq \$c, %rax</code>	Subtract constant <code>c</code> from <code>rax</code> register
<code>subq %rax, %rbx</code>	Subtract <code>rax</code> register from <code>rbx</code> register
<code>cmpq \$0, %rdx</code>	Compare constant <code>0</code> register against <code>rdx</code> register
<code>cmpq %rax, %rdx</code>	Compare <code>rax</code> register against <code>rdx</code> register
<code>movq %rax, (%rbx)</code>	Assign <code>rax</code> register to dword at address <code>rbx</code>
<code>movq (%rbx), %rax</code>	Assign <code>rax</code> register from dword at address <code>rbx</code>
<code>movq 4(%rsp), %rax</code>	Assign <code>rax</code> register from dword at address <code>rsp+4</code>
<code>movq %rdx, (%rsi, %rbx, 4)</code>	Assign <code>rdx</code> register to dword at address <code>rsi+4*rbx</code>
<code>pushq %rax</code>	Push <code>rax</code> register onto stack
<code>pushq %c</code>	Push constant <code>c</code> onto stack
<code>popq %rdi</code>	Pop qword off stack and assign to register <code>rdi</code>
<code>jz target</code>	Branch to <code>target</code> if zero flag set.
<code>jnz target</code>	Branch to <code>target</code> if zero flag not set.
<code>jl target</code>	Branch to <code>target</code> if less than (i.e. sign flag set).
<code>jle target</code>	Branch to <code>target</code> if less than or equal (i.e. sign or zero flags set).
<code>ret</code>	Return from function.

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