

Advanced Electronics - Course Outline

ECEN 403: 2014 Trimester 2

This document sets out the workload and assessment requirements for ECEN 403. It also provides contact information for staff involved in the course. If the contents of this document are altered during the course, you will be advised of the change by an announcement in lectures and/or on the course web site. A printed copy of this document is held in the School Office.

This course discusses advanced analogue electronics principles mainly from high frequency analysis and amplification perspectives. The first part of the course is mainly laboratory based analysing different aspects of op-amp design. The second part of the course is mainly analysing small signal Transistor amplifiers using two port and small signal analysis. High frequency response of some common amplifiers will also be discussed.

Objectives (and associated graduate attributes)

By the end of the course, students will be able to:

1. Analyse the small signal modeling methods of devices and circuits including device parasitic capacitances 3(c)
2. Analyse the frequency response of the circuits 3(c)
3. Design and analyse common BJT circuit elements 3(a) 3(b) 3(c)
4. Measure common analogue circuit imperfections 3(c)
5. Use the spice simulator to design and analysis of BJT and FET circuits 3(c) 3(f)

Textbook

Sedra and Smith, Microelectronic circuits, sixth edition, Oxford University Press, 2010 (good for small signal BJT/MOS models)

The following books are useful for this course:

General

1. Horowitz and Hill, The Art of Electronics, (2nd edition), Cambridge University Press, 1989 (good for practical techniques).

Course Content

The following is an outline of the topics covered in the lectures:

- Small Signal Modelling (SSM)
- SSM of MOSFETS
- SSM of Diodes
- SSM of BJTs *Two Port Networks *Introduction to RF (Week 7)
- Frequency Response

The Class Representative is **Eden Rafealov**

Lectures, Tutorials, Laboratories, and Practical work

A schedule of lecture topics, readings, and assignment due dates is available online and will be populated during the course

Lectures for ECEN 403 are: Monday, Tuesday and Thursday at 4:10 pm in Murphy 631

Labs for ECEN 403 are: Labs will be between 11 and 2 on Tuesday in CO250. Most labs will be able to be completed in AM218/9 at other times.

Assignments

There will be two written assignments due during weeks eight and ten.

Workload

In order to maintain satisfactory progress in ECEN 403, you should plan to spend an average of at least 10 hours per week on this paper.

In the first portion of the course, a plausible and approximate breakdown for these hours would be:

- Lectures and tutorials: 1 hours/week
- Laboratories: 6 hours/week
- Reading and research: 3 hours/week

For the second part of the course, a plausible and approximate breakdown for these hours would be:

- Lectures and tutorials: 3 hours/week
- Laboratories and assignments: 6 hours/week
- Readings: 1 hours/week

2.5hrs/week is assigned for preparation towards tests and the exam.

School of Engineering and Computer Science

The School office is located on level three of the Cotton Building ([Cotton 358](#)).

Staff

The course organiser for ECEN 403 is [Ramesh Rayudu](#). The lecturers for the course are [Christopher Hollitt](#) and [Ramesh Rayudu](#). Their contact details are:

- Christopher Hollitt
- [AM223](#)
- Ext 6965
- Christopher.Hollitt@vuw.ac.nz etc

- Ramesh Rayudu
- [Cotton 352](#)
- Ext 8068
- ramesh.rayudu@ecs.vuw.ac.nz etc

- Mark Walker
- [AM221](#)
- mark.walker@ecs.vuw.ac.nz etc

Announcements and Communication

The main means of communication outside of lectures will be the ECEN 403 web area at http://ecs.victoria.ac.nz/Courses/ECEN403_2014T2/. There you will find, among other things, this document, and the [lecture schedule](#).

Assessment

Your grade for ECEN 403 will be determined based on the following assessment weightings:

Item	Weight	Objectives
2 Transistor lab reports (wk TBD)	20%	3-5
Midterm Test (Data TBD)	30%	3
2 laboratory Reports (wk 7,9)	10%	1,2,4,5
2 Assignments (wk 8,10)	10%	1,2,4
Final Examination (3 hours)	30%	1-2

Bachelor of Engineering students should be aware that copies of their assessed work may be retained for inspection by an IPENZ appointed accreditation panel.

Policies and penalties for late submission

The printed results of each laboratory session are to be handed in no later than one week following the laboratory session. Assignments and labs not collected in lectures can be collected from the SECS school office. Late submission of lab work incurs a penalty of 10% per working day.

Both written assignments will be counted equally to the 10% assignment mark.

Late submissions will be penalised at 10% per day or part therefore.

Tests and Exams

The timetable for final examinations will be available from the University web site and will be posted on a notice board outside the faculty office. The final examination will be three hours long. No computers, electronic calculators or similar device will be allowed in the final examination. Paper non-English to English dictionaries will be permitted. The examination period for trimester 2 is 24 October - 15 November.

Plagiarism

Working Together and Plagiarism

We encourage you to discuss the principles of the course and assignments with other students, to help and seek help with programming details and problems involving the lab machines. However, any work you hand in must be your own work.

The School policy on Plagiarism (claiming other people's work as your own) is available from the course home page. Please read it. We will penalise anyone we find plagiarising, whether from students currently doing the course, or from other sources. Students who knowingly allow other students to copy their work may also be penalised. If you have had help from someone else (other than a tutor), it is always safe to state the help that you got. For example, if you had help from someone else in writing a component of your code, it is not plagiarism as long as you state (eg, as a comment in the code) who helped you in writing the method.

Mandatory Requirements

To pass ECEN 403, a student must satisfactorily :

1. Achieve at least 40% in the test and the final examination and
2. Submit a minimum of 3 out of 4 lab reports

Passing ECEN 403

To pass ECEN 403, a student must satisfy mandatory requirements and gain at least a **C-** grade overall.

Withdrawal

The last date for withdrawal from ECEN 403 with entitlement to a refund of tuition fees is Friday 25 July 2014. The last date for withdrawal without being regarded as having failed the course is Friday 26 September 2014 -- though later withdrawals may be approved by the Dean in special circumstances.

Course Feedback

The course feedback for ECEN403 is available [here](#).

Rules & Policies

Find key dates, explanations of grades and other useful information at <http://www.victoria.ac.nz/home/study>.

Find out about academic progress and restricted enrolment at <http://www.victoria.ac.nz/home/study/academic-progress>.

The University's statutes and policies are available at <http://www.victoria.ac.nz/home/about/policy>, except qualification statutes, which are available via the Calendar webpage at <http://www.victoria.ac.nz/home/study/calendar> (See Section C).

Further information about the University's academic processes can be found on the website of the Assistant Vice-Chancellor (Academic) at <http://www.victoria.ac.nz/home/about/avcacademic>

All students are expected to be familiar with the following regulations and policies, which are available from the school web site:

Grievances

Student and Staff Conduct

Meeting the Needs of Students with Disabilities

Student Support

Academic Integrity and Plagiarism

Dates and Deadlines including Withdrawal dates

School Laboratory Hours and Rules

Printing Allocations

Expectations of Students in ECS courses

The School of Engineering and Computer Science strives to anticipate all problems associated with its courses, laboratories and equipment. We hope you will find that your courses meet your expectations of a quality learning experience.

If you think we have overlooked something or would like to make a suggestion feel free to talk to your course organiser or lecturer.

[Course Outline as PDF](#)
